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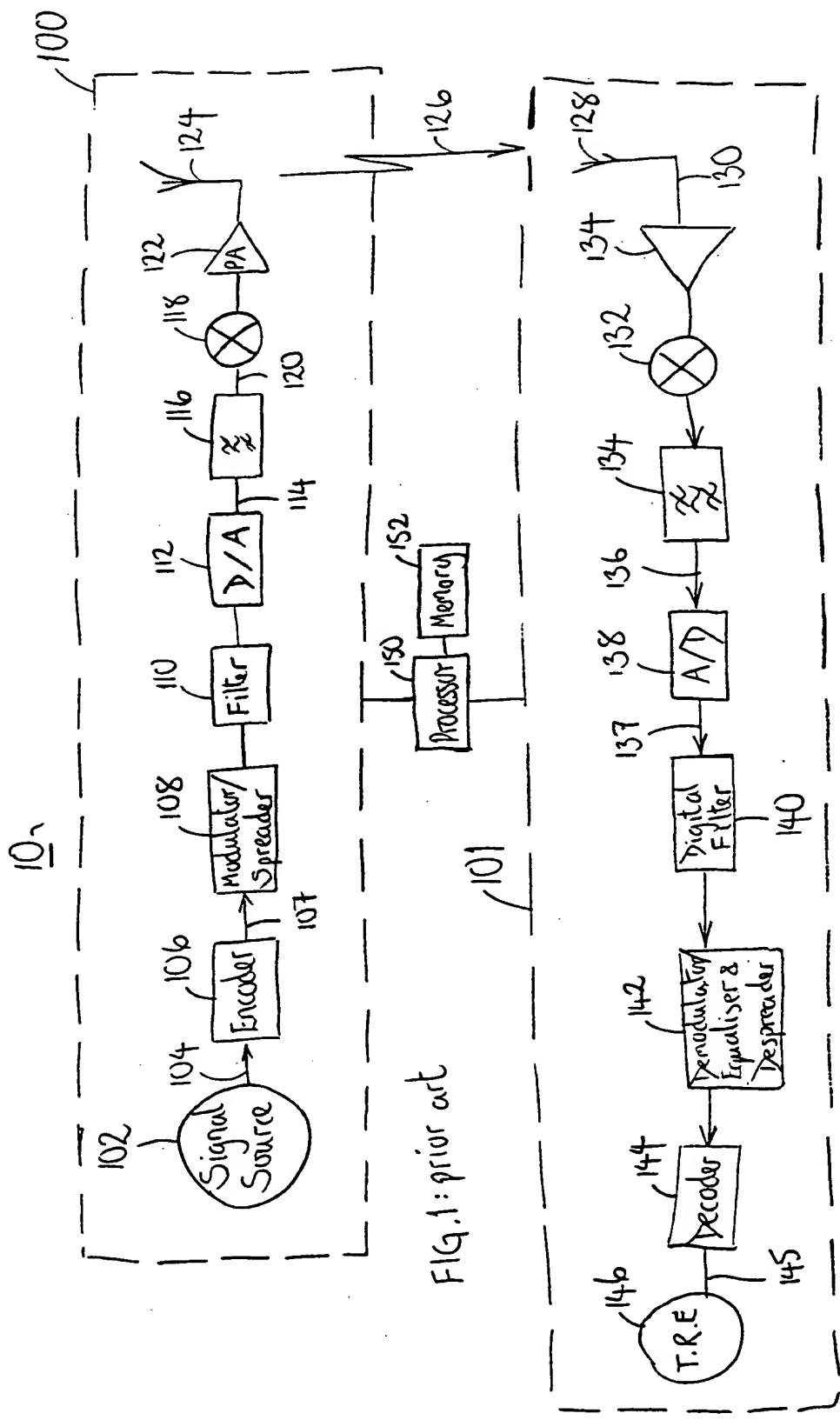
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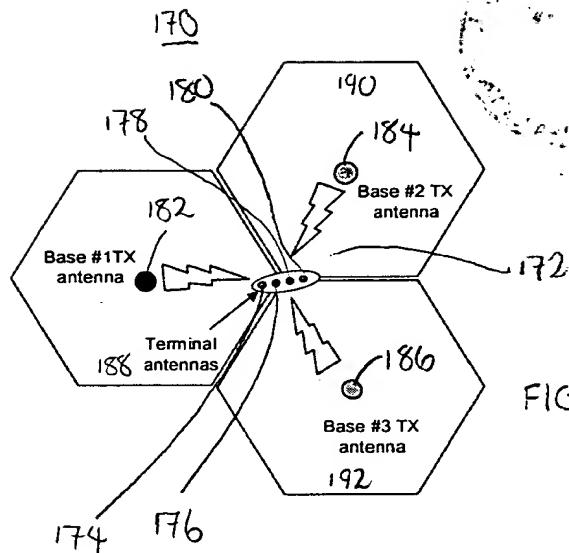


FIG. 2: prior art

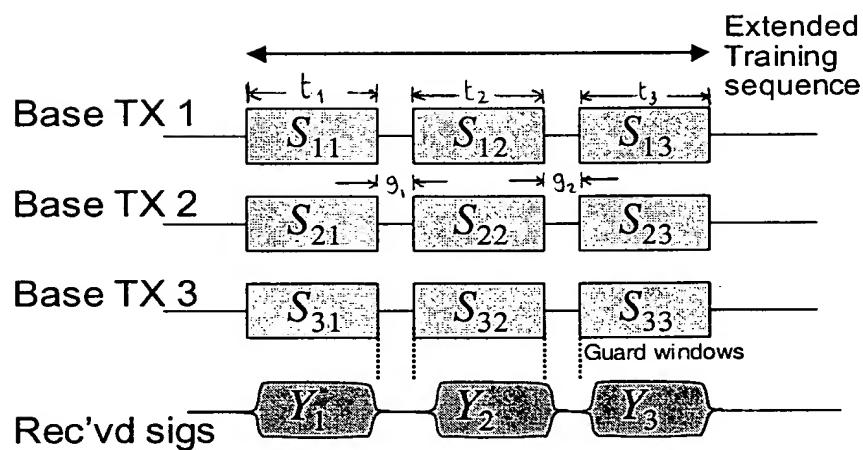


FIG. 4

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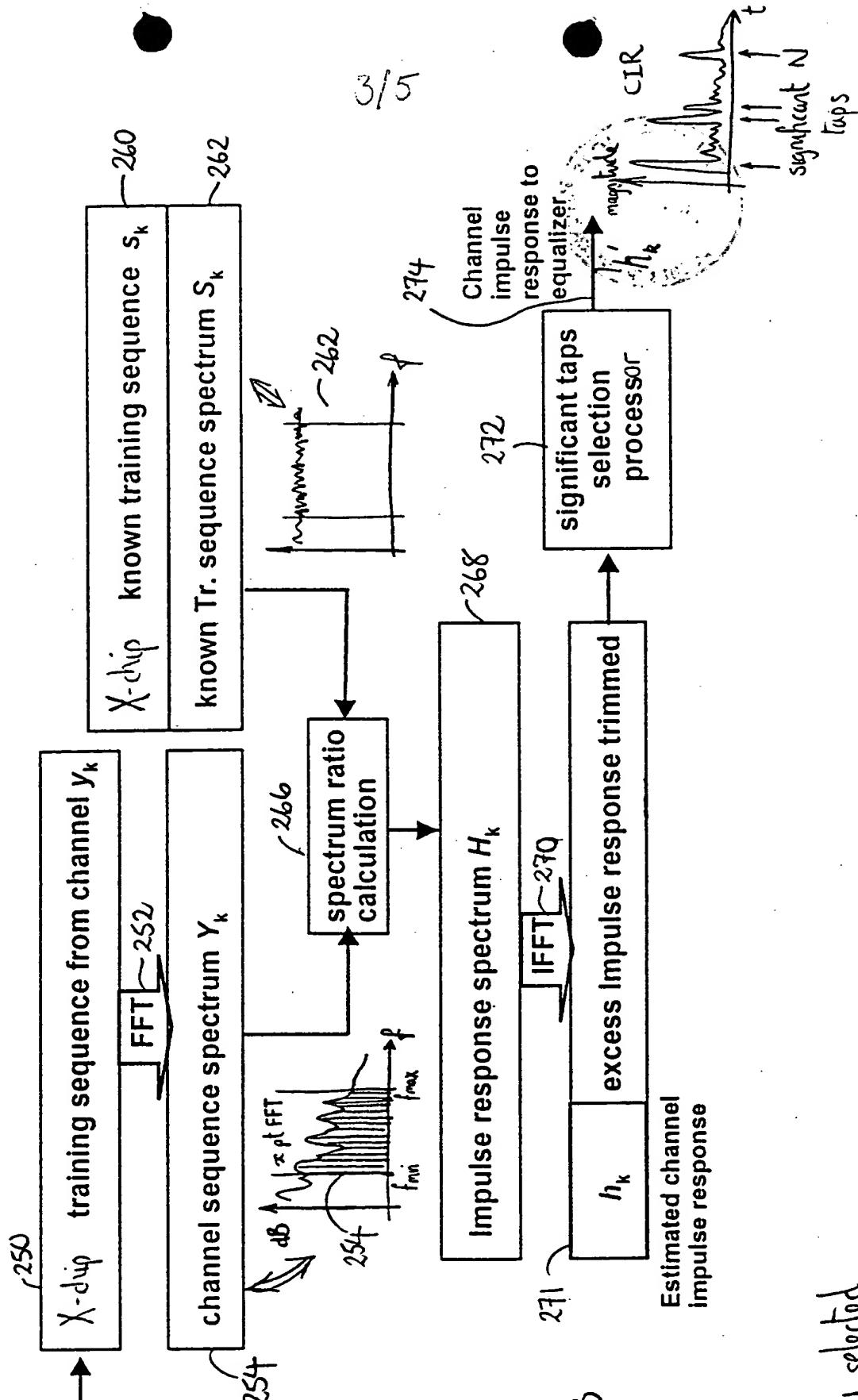


FIG.3

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$\chi$  = arbitrary selected number of chips in training sequence set by system standard.

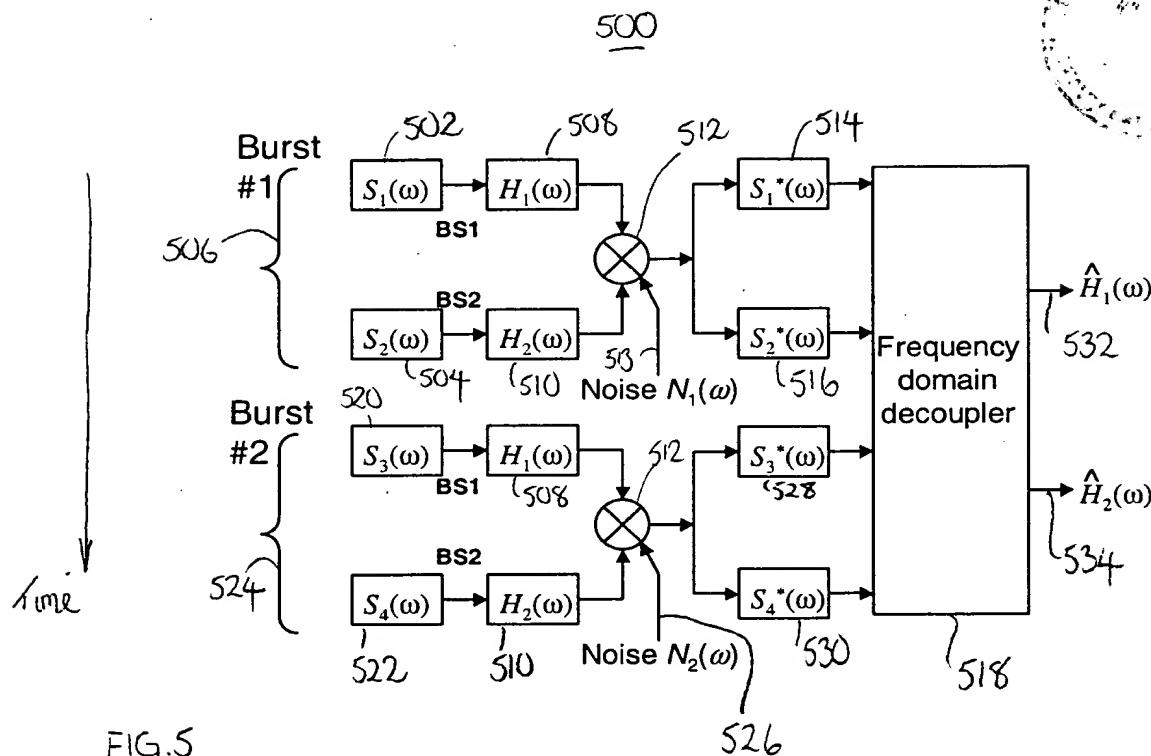


FIG. 5

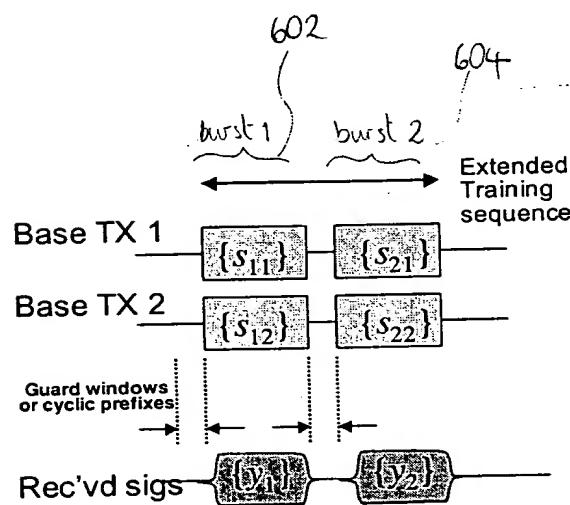


FIG. 6

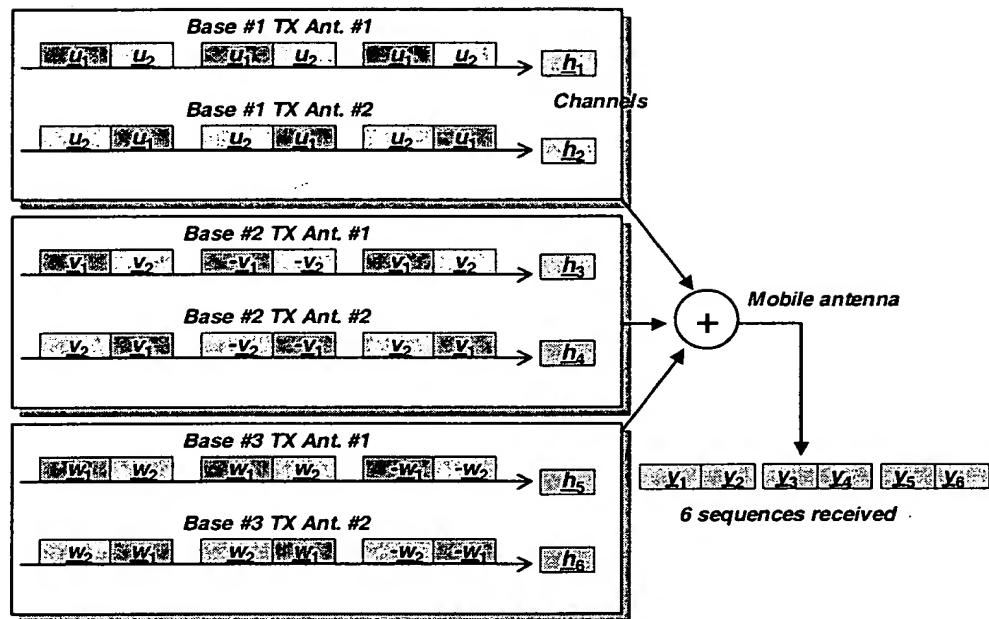


FIG.7

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